Docket No.: M4065.0644/P644

Title: RESISTANCE VARIABLE MEMORY ELEMENTS BASED ON POLARIZED SILVER-SELENIDE NETWORK GROWTH

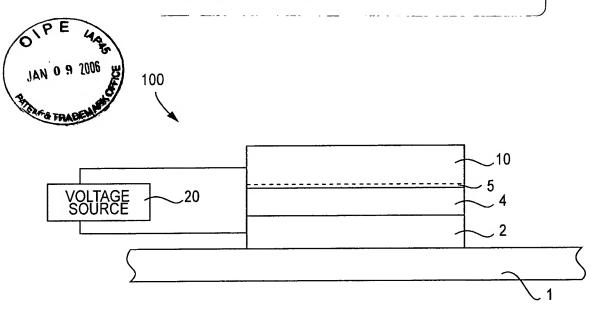


FIG. 1A

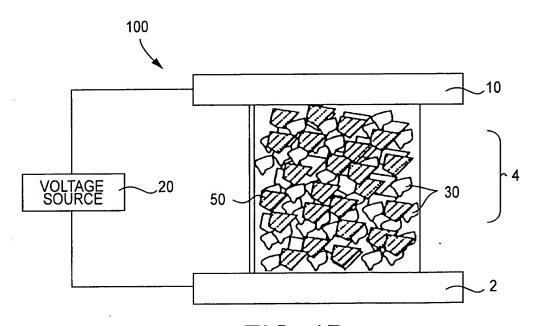
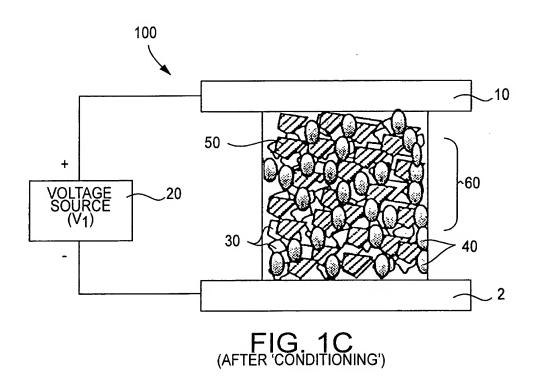
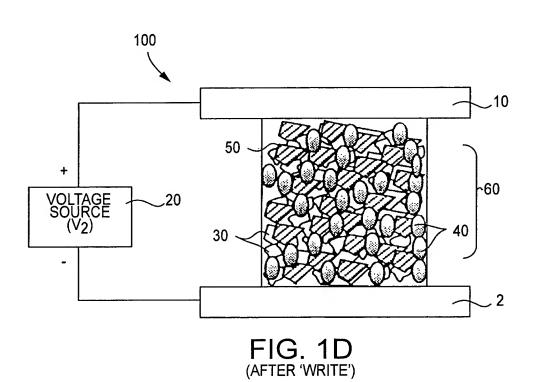


FIG. 1B (PRIOR TO 'CONDITIONING' OR 'WRITE' PULSE)

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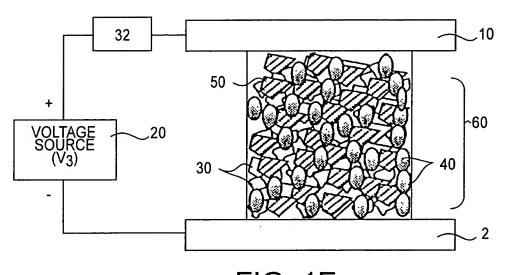


FIG. 1E (READ-OUT VOLTAGE)

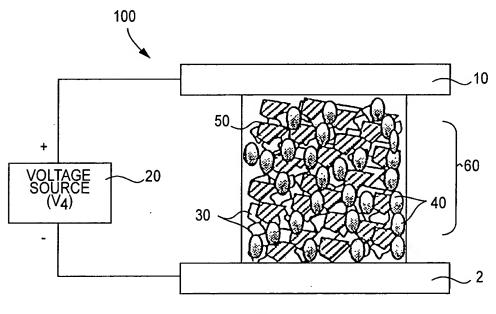


FIG. 1F (AFTER 'ERASE')

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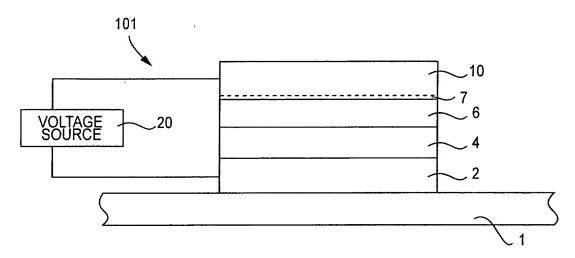


FIG. 2A

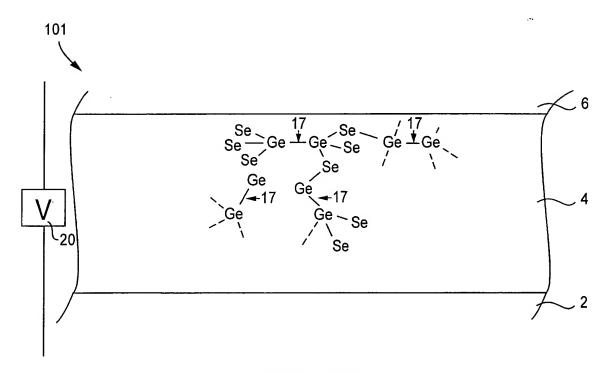


FIG. 2B

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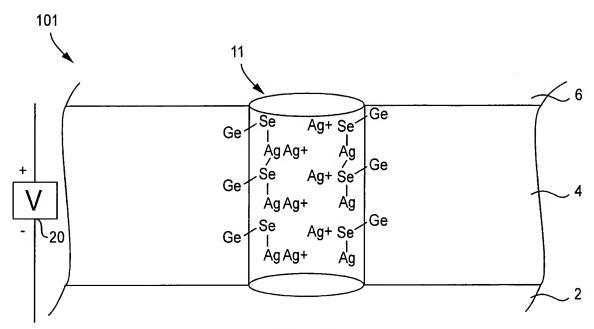


FIG. 2C

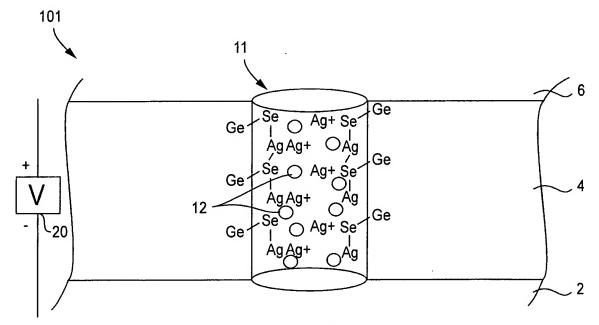


FIG. 2D

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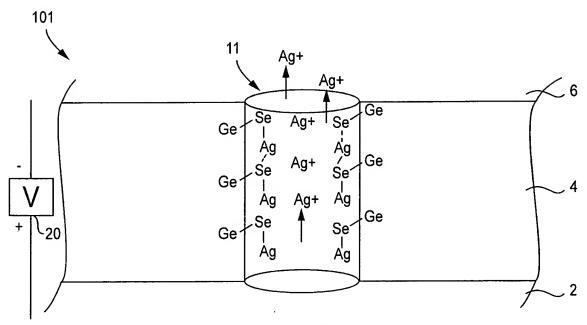


FIG. 2E

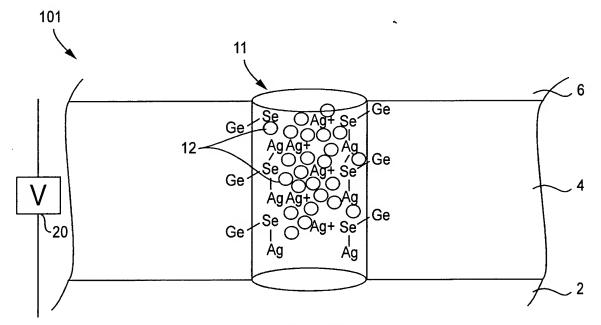


FIG. 2F

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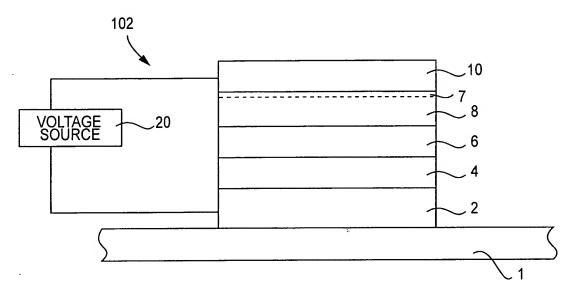


FIG. 3A

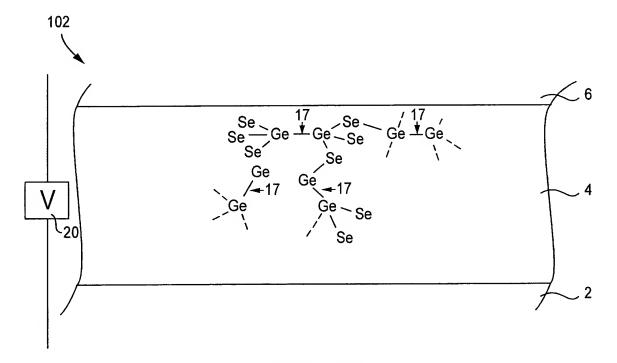


FIG. 3B

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FIG. 3C

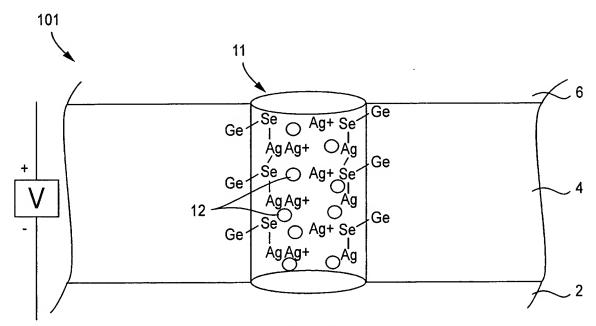


FIG. 3D

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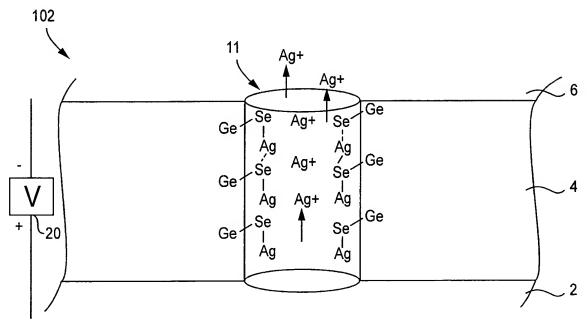


FIG. 3E

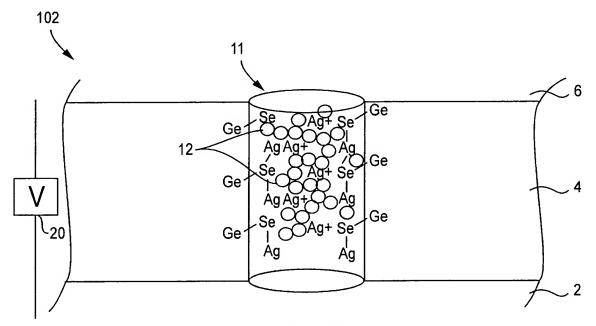
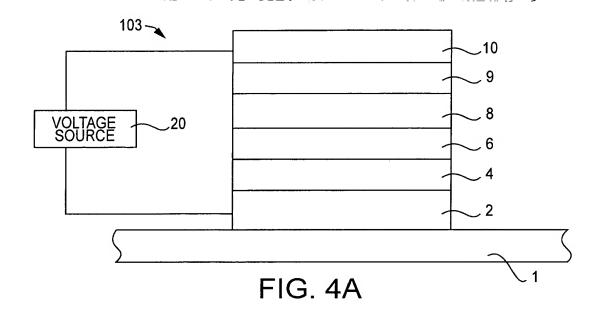


FIG. 3F

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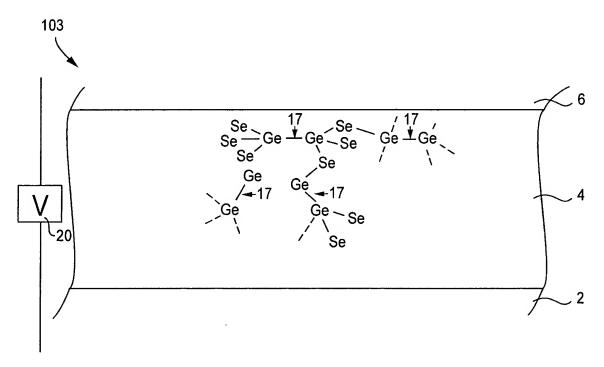


FIG. 4B

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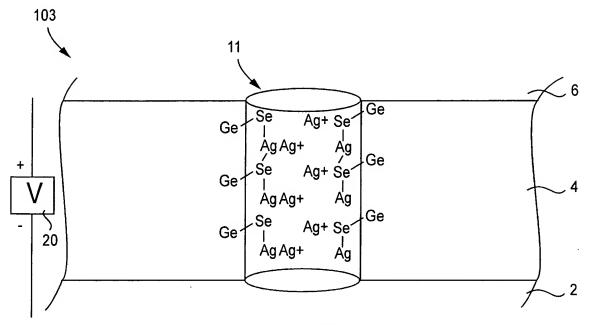


FIG. 4C

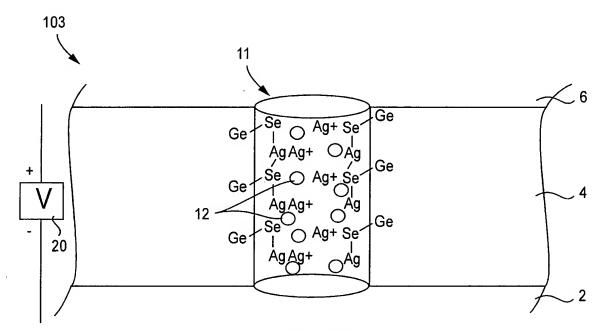


FIG. 4D

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Title: RESISTANCE VARIABLE MEMORY ELEMENTS BASED ON POLARIZED SILVER-SELENIDE NETWORK GROWTH

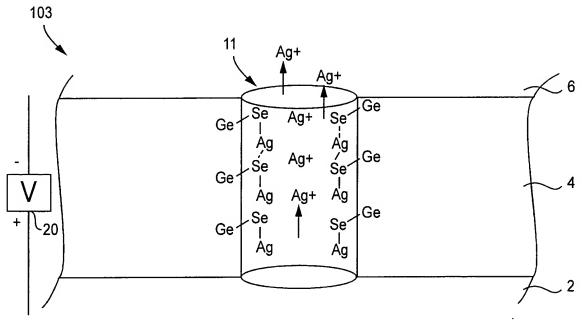


FIG. 4E

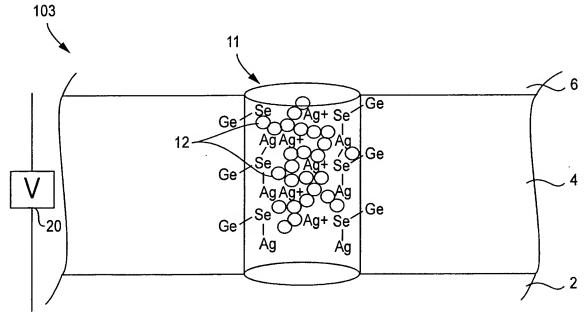


FIG. 4F

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Inventor: Meri Helms
Title: RESISTANCE VARIABLE MEMORY ELEMENTS BASED
ON POLARIZED SILVER-SELENIDE NETWORK GROWTH

